



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

File the Application of

Hisaki KURASHINA

Application No.: 10/680,296

Docket No.: 110826.02

Filed: October 8, 2003

For: ELECTRO-OPTICAL DEVICE HAVING STORAGE CAPACITOR LAMINATED
BETWEEN TFT AND DATA LINE

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
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Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,


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Date: January 28, 2004

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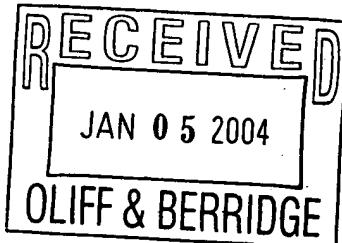


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/680,296	10/08/2003	2871	770	110826.02	40	8	1

25944
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CONFIRMATION NO. 4228

FILING RECEIPT



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Date Mailed: 01/02/2004

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Applicant(s)

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Assignment For Published Patent Application

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Tokyo

Domestic Priority data as claimed by applicant

This application is a DIV of 10/397,220 03/27/2003 PAT 6,665,024
 which is a DIV of 10/005,147 12/07/2001 PAT 6,597,413
 which is a CON of PCT/JP01/03360 04/19/2001

Foreign Applications

JAPAN 2000-121452 04/21/2000

Please
 see both
 pages

If Required, Foreign Filing License Granted: 01/02/2004**Projected Publication Date: 04/15/2004****Non-Publication Request: No****Early Publication Request: No****Title**

Electro-optical device having storage capacitor laminated between TFT and data line

Capacitor

Preliminary Class

349

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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